Subject: Time protocols used for SADC

Posted by Walter F.J. Müller on Sat, 01 May 2004 19:01:27 GMT

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In the thread How to interface SADC to Time distribution is described by Lars and Igor how the SADC is conceptually connected to the time distribution.

I wonder whether there is somewhere a more detailed documentation which describes: how the TTC is used with the SADC in self-triggered operation what time representation is distributed in this case by the TTC what the hit and time stamp format is in the SADC data stream

Subject: Re: Time protocols used for SADC Posted by Igor Konorov on Sun, 02 May 2004 17:21:02 GMT View Forum Message <> Reply to Message

There is a diploma thesis of Alexander Mann which describes the SADC and one of a method to extract the timing information. But it might be faster if I answer:

1. in the triggered mode the time of the signal is referenced to the trigger time and in self triggered mode it is referenced to a time ZERO. The time ZERO is defined by the TCS when it broadcasts the RESET signal. The trigger and the RESET signals are synchronous to the TCS clock.

Each frontend module including SADC counts the TCS clock. The counter is reseted by the TCS RESET signal. In this case the value of the counter corrected by the propagation time of the clock through the fibre is an absolute time or a time stamp.

2. the SADC detects the signal, calculates the amplitude and the time. The time is given by the clock counter(time stamp) after which the signal appeared and by 5 bits which define the timing of the signal respectively to the clock phase within the clock period.

Subject: Re: Time protocols used for SADC Posted by Walter F.J. Müller on Mon, 03 May 2004 17:04:13 GMT View Forum Message <> Reply to Message

Igor Konorov wrote on Sun, 02 May 2004 19:21There is a diploma thesis of Alexander Mann

Thanks. Is this diploma thesis somewhere on the web?

And to understand it more quantitatively:

How often is the RESET signal send by the TCS

How many bits does the clock counter have, and are all these bits transmitted in each time stamp for each hit.

Subject: Re: Time protocols used for SADC Posted by Igor Konorov on Thu, 06 May 2004 14:03:06 GMT

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the theses can be found in

http://www.e18.physik.tu-muenchen.de/research/publications/compass/Data_ Acquisition_and_Fast_Signal_Processing_for_a_new_Positron_Emission_Tomog raph.pdf

- 1. the TCS controller sends RESET every 14 seconds. This time interval came from SPS duty cycle.
- 2. there are different implementations of the counter. In current version it has 30 bits.