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Subject: Re: PCIe-AS - What are limitations on Network size ?

Posted by [Walter F.J. Müller](#) on Wed, 15 Sep 2004 09:10:03 GMT

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Walter F.J. Müller wrote on Tue, 14 September 2004 15:47: The PNet based on 3 cascaded 8-port switches shown in my talk needs only a 9 bit path address.

This isn't correct. Since 4 8-port switches have to be traversed, see the attached figure, one needs  $4 \times 3 = 12$  bits.

At this place another correction: In my talk I mistakenly wrote that the PEX 8532 is a AS switch. It is a PCIe switch, a true AS switch is announced.

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### File Attachments

1) [4switch.png](#), downloaded 1207 times

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