Subject: PCIe-AS - Tutorial -- Is there a CRC for the address header ?
Posted by Walter F.J. Müller   on Tue, 14 Sep 2004 07:54:59 GMT

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During the 2nd FutureDAQ workshop (all talks in document management) some questions came up during E. Denes's talk on PCIe-AS. On the Intel PCIe website is a very good and comprehensive tutorial talk given at the PCI SIG Developers Conference 2004, see

ftp://download.intel.com/netcomms/as/devcon_as_overview.pdf

On 116 slides, many aspects of the protocol are described in quite some detail.

Now to a specific question raised during the talk:

Q: Is there a CRC protection for the address header ?

A: Yes, see on page 14 of the tutorial. The AS header is 64 bits, which include
7 bit header CRC
5 bit turn pointer
7 bit PI number
31 bit turn pool
1 bit direction (forward/backward routing)