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Subject: Re: Energy loss in this silicon layer  
Posted by [Stefano Spataro](#) on Tue, 06 May 2008 09:10:18 GMT  
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Dear all,  
I put here my slide with the dE/dx plots for g3 and g4 using the flag SetSTRA(0), for pions and electrons (MVD geometry).

As you can see, whit this option the discrepancy vanishes.

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#### File Attachments

1) [mvdpid\\_stra0.pdf](#), downloaded 619 times

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