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Subject: Dynamic re-configuration of FPGA's

Posted by [Walter F.J. Müller](#) on Mon, 24 May 2004 18:24:43 GMT

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In a recent CBM DAQ/Trigger meeting Prof. Udo Keschull from KIP Heidelberg presented some interesting usage modes of the internal configuration interface of Xilinx Virtex FPGAs. One highlight is to make the FPGA resources transparently accessible via a Linux /proc file system.

The presentation is accessible from the CBM Document Management.

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