
Subject: Re: Raw data read and write
Posted by [Ken Oyama](#) on Wed, 06 Dec 2006 17:17:33 GMT
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Hi Christian, and all,

Adding TRAP pedestal of 10 might be enough if we do it carefully.

What will be different from real case is that if ADC intrinsic pedestal is large (like 100), we loose dynamic range of 10 % because ADC range is limited from 0 to 1023.

This feature can not be simulated if we add 10. It seems designer of current digitizer was thinking about this effect as far as I see from the code.

This problem is relevant only if there is huge signal. I do not know this affects to tracking performance. Maybe it is negligible. Does anybody know?

If we decide such effect is negligible and do not care then I would just put 10 (actually it was done!).

best regards,

Ken
