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Subject: Re: LYCCA - w-DSSSD Time Gated  
Posted by [thuyuk](#) on Wed, 13 Aug 2014 16:57:04 GMT  
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Hi Liliana,

Many thanks for sharing the related part of the config file.

One question: In the DSSD module 12, the cable assignment doesn't follow the trend, e.g.:

```
amplitude_p[0:13] <- LyccaWallCrate1.adc12[0:13]
amplitude_p[15] <- LyccaWallCrate1.adc12[15]
amplitude_n[0:15] <- LyccaWallCrate1.adc12[16:31]
```

do you know if the same applies for the time signals?

Thanks!  
Tayfun

edit: Sorry! I just saw that only strip #14 is missing. I certainly can handle this! Please ignore this message.

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