
Subject: LYCCA - w-DSSSD Time Gated
Posted by [thuyuk](#) on Wed, 13 Aug 2014 10:48:08 GMT
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Hi all,

I would like to make use of LYCCA.DSSDTimeGated processor but I have some information missing: I couldn't locate the time signals of the p-side of the silicons in Crates.config. Would you be so kind to help me to update my config files?

Thank you very much in advance!

Tayfun
